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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Toshimichi Naruse et al. Art Unit : Unknown
Serial No. : 10/813,189 Examiner : Unknown
Filed : March 29, 2004
Title : HYBRID INTEGRATED CIRCUIT DEVICE

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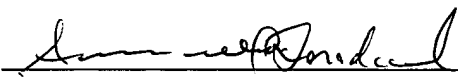
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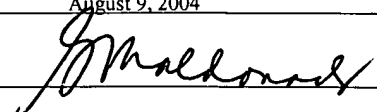
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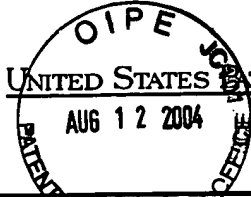
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Applicant(s)

Toshimichi Naruse, Residence Not Provided;
Nobuhisa Takakusaki, Residence Not Provided;
Hajime Kobayashi, Residence Not Provided;

Domestic Priority data as claimed by applicant**Foreign Applications**

JAPAN 2003-188523 06/30/2003

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Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

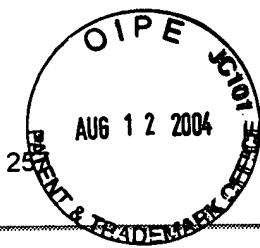
Early Publication Request: No

TitleHybrid integrated circuit *Device***Preliminary Class**

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